



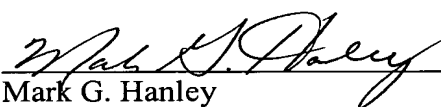
IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Gyung-Su Cho  
U.S. Serial No. : 10/676,645  
Filed : October 1, 2003  
Title : "Semiconductor Device  
and Fabrication Method  
Thereof"

Art Unit : 2811  
Examiner : Thomas J. Magee

) I hereby certify that this paper (and/or  
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) 1450, Alexandria, VA 22313-1450 on  
) this date:

) **Dated: December 22, 2004**

)   
) Mark G. Hanley  
) Registration No. 44,736  
) Attorney for Applicant

**RESPONSE TO THE OFFICE ACTION DATED NOVEMBER 5, 2004**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

In response to the Office action dated November 5, 2004, please enter the  
following amendments and consider the following remarks:

**Amendments** begin on page 2 of this paper.

**Remarks** begin on page 4 of this paper.